

Call for Participation

HPCRI: 1st Workshop on High Performance Computing Reliability Issues

To be held in conjunction with
[the 11th International Symposium on High Performance Computer
Architecture \(HPCA-11\)](#)

Palace Hotel, San Francisco
February 12-16, 2005

- 8:30 – 9:30 ***Key Note by Dr. Ziegler***
- 9:30 to 10:00 *Montecito Error Protection and Mitigation*
Cameron McNairy, Jenna Mayfield - Intel Corporation
- 10:00 to 10:30 **BREAK**
- 10:30 to 11:00 *Using k+2 RAID and Delayed Rebuilds to Address Performance and
Data Loss in a Petabyte Scale Parallel File System*
Ian R. Philp - Los Alamos National Lab
- 11:00 to 11:30 *Proactive Fault Tolerance in Large Systems*
Sayantan Chakravorty Celso L. Mendes Laxmikant V. Kal'e - UIUC
- 11:30 to 12:00 *Fault Aware Instruction Placement for Static Architectures*
Premkishore Shivakumar Divya P. Gulati Calvin Lin Stephen W. Keckler
– Univ. Of Texas at Austin
- 12:00 to 1:00 **LUNCH**
- 1:00 to 1:30 *Evaluating the Performance of a Checkpointing Application Given the
Number and Types of Interrupts*
John T. Daly - Los Alamos National Laboratory
- 1:30 to 2:00 *Banking Chip Lifetime: Opportunities and Implementation*
Zhijian Lu, John Lach, Mircea Stan, Kevin Skadronx -University of
Virginia
- 2:00 to 2:30 *Software Failures and the Road to a Petaflop Machine*
Ian R. Philp - Los Alamos National Lab
- 2:30 to 3:00 *Afternoon BREAK*
- 3:00to 3:30 *SOS: Using Speculation for Memory Error Detection*
Sudhanva Gurumurthi Angshuman Parashar Anand Sivasubramaniam –
Pennsylvania State University
- 3:30 to 5:00 **Panel Discussion: *Challenges in Computing Availability and Reliability
over the Next Five Years*** (HP, IBM, Intel, UIUC, LANL, PNNL)